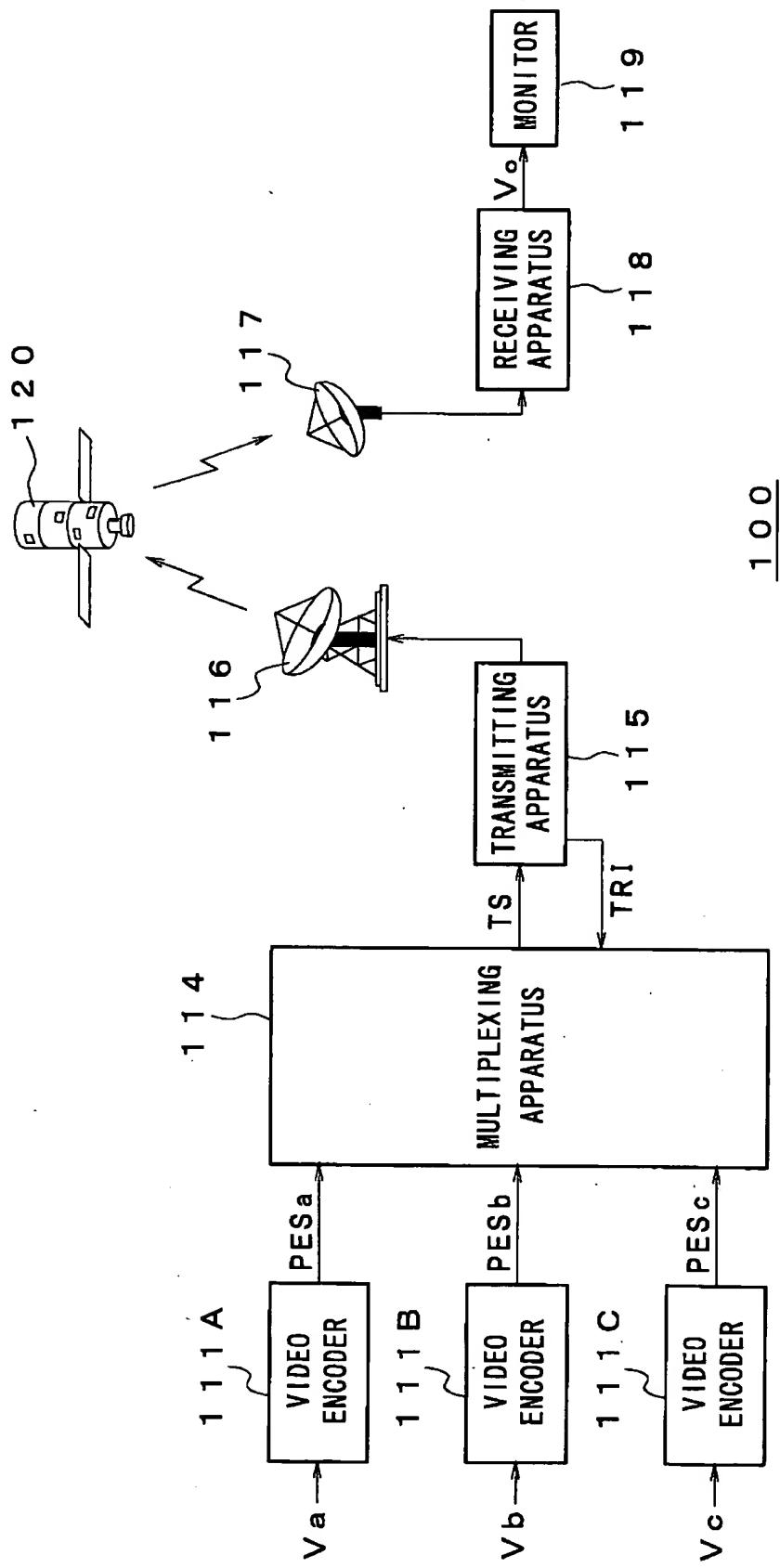


599P12714500

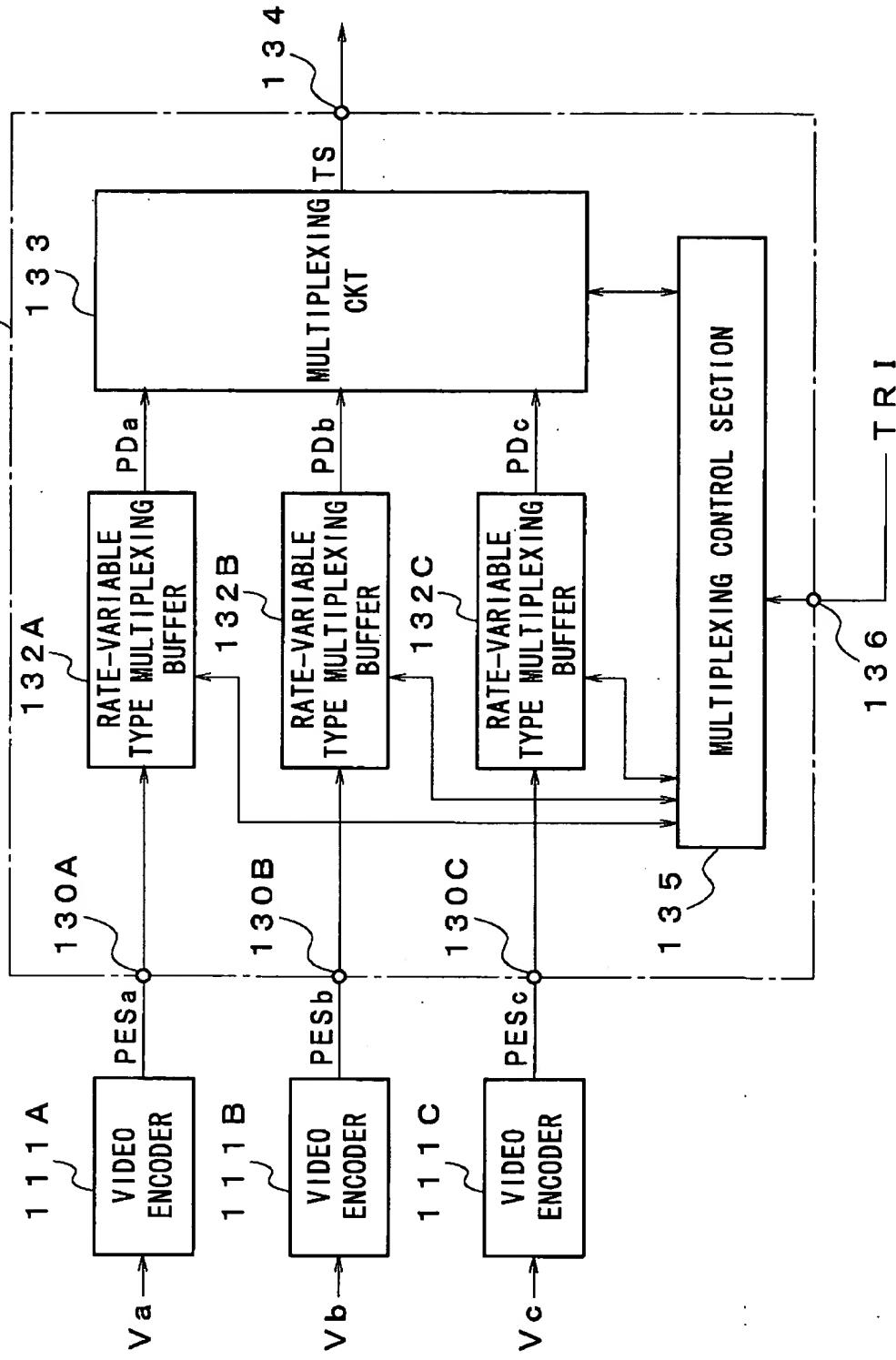
1 / 1 4

FIG. 1



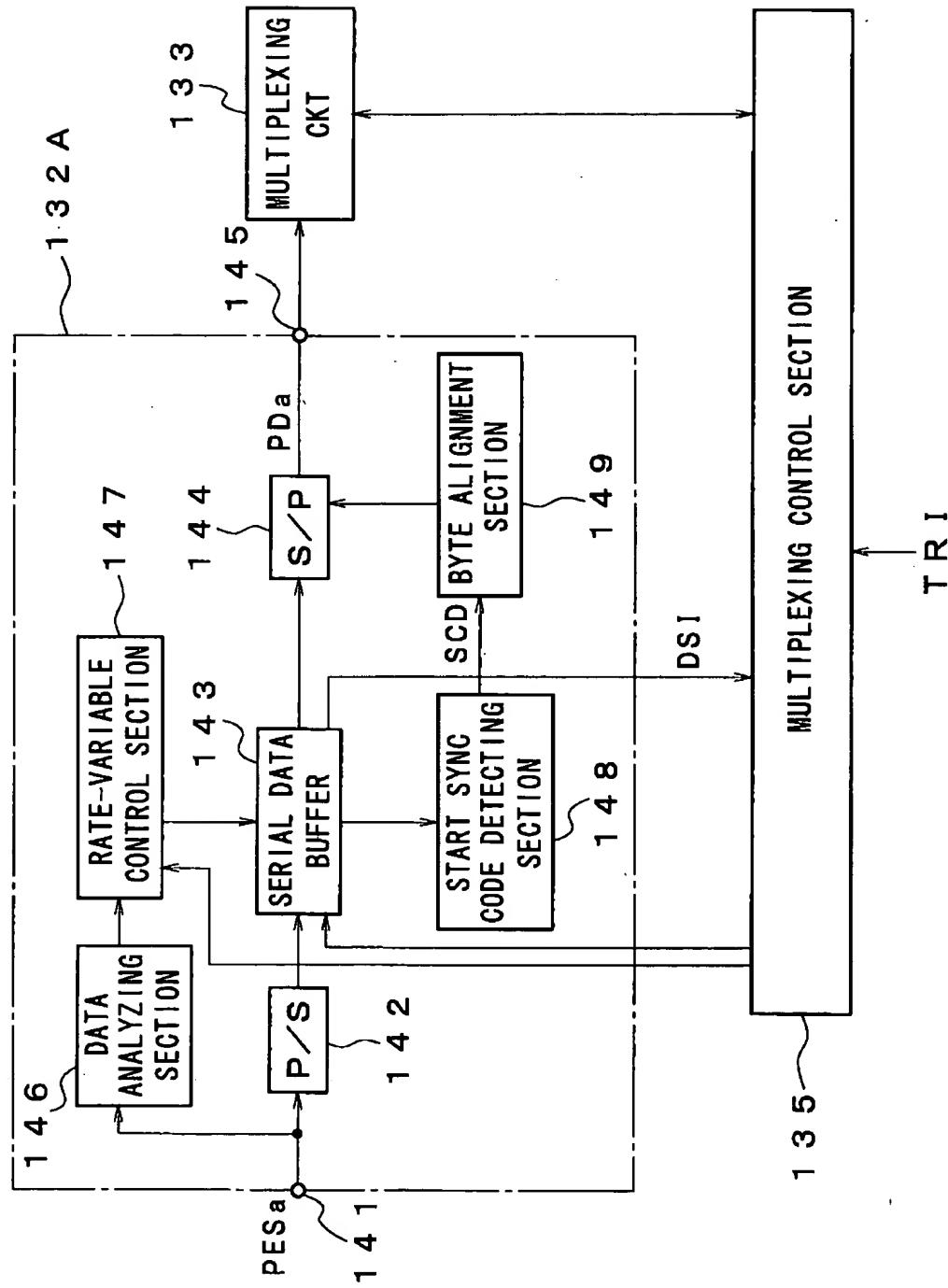
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FIG. 2



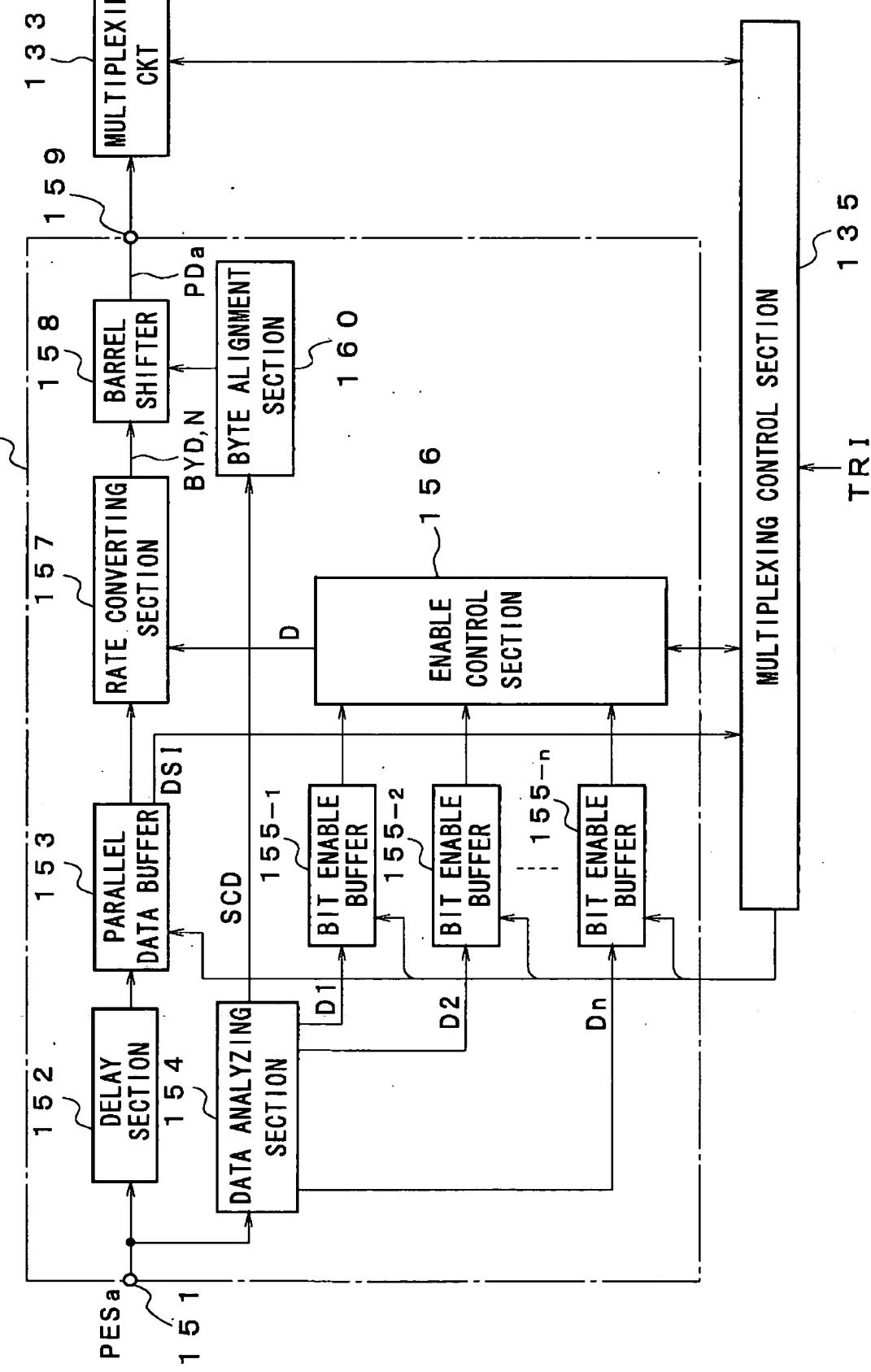
3 / 1 4

3  
FIG



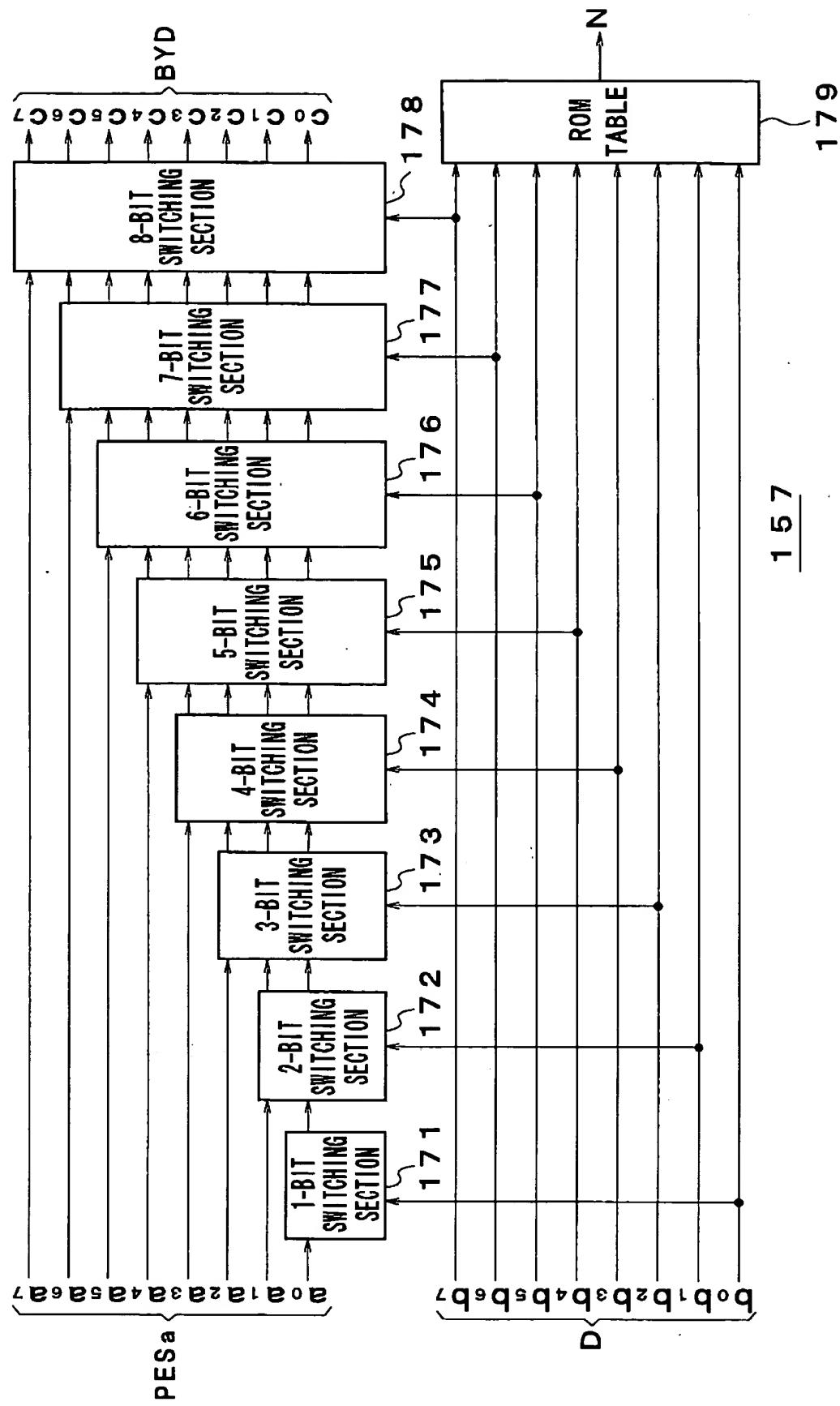
4 / 1 4

FIG. 4



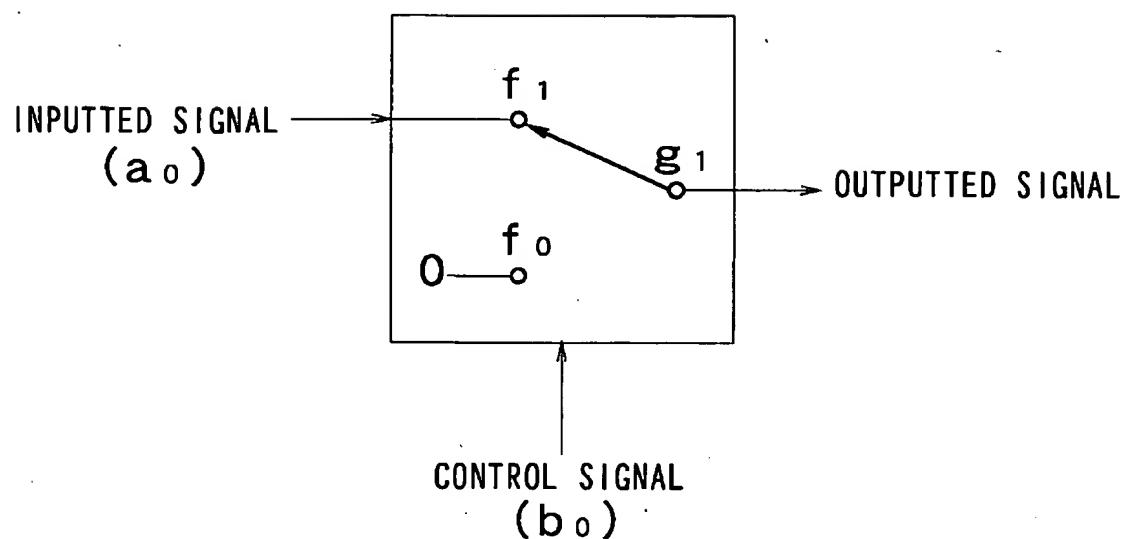
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FIG. 5



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## F I G . 6

1 7 1

## F I G . 7

INPUTTED SIGNAL ( $a_0$ )	CONTROL SIGNAL ( $b_0$ )	OUTPUTTED SIGNAL
0	0	0
1	0	0
0	1	0
1	1	1

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FIG. 8

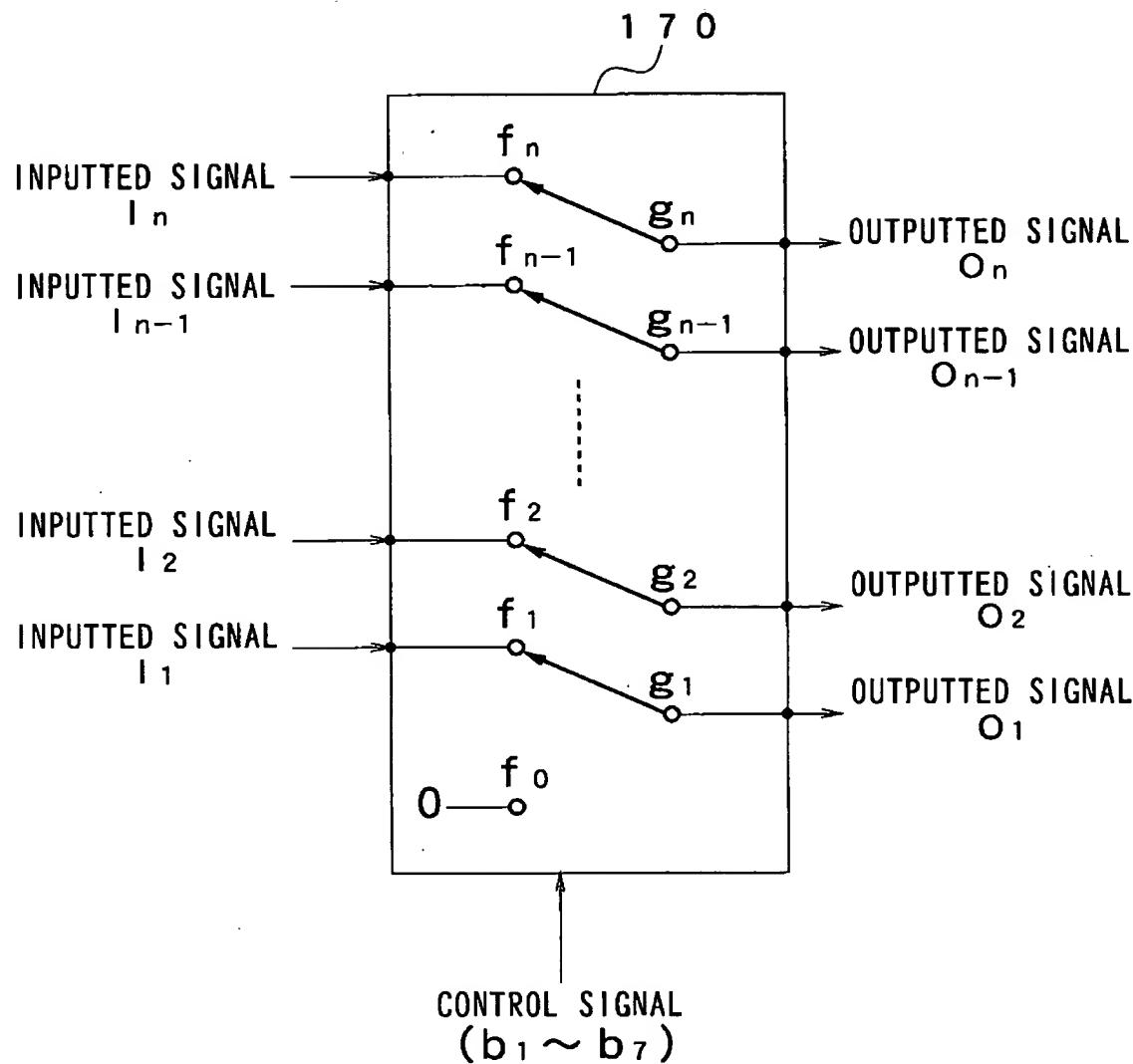


FIG. 9

CONTROL SIGNAL	OUTPUTTED SIGNAL $O_n$
0	$I_{n-1}$
1	$I_n$

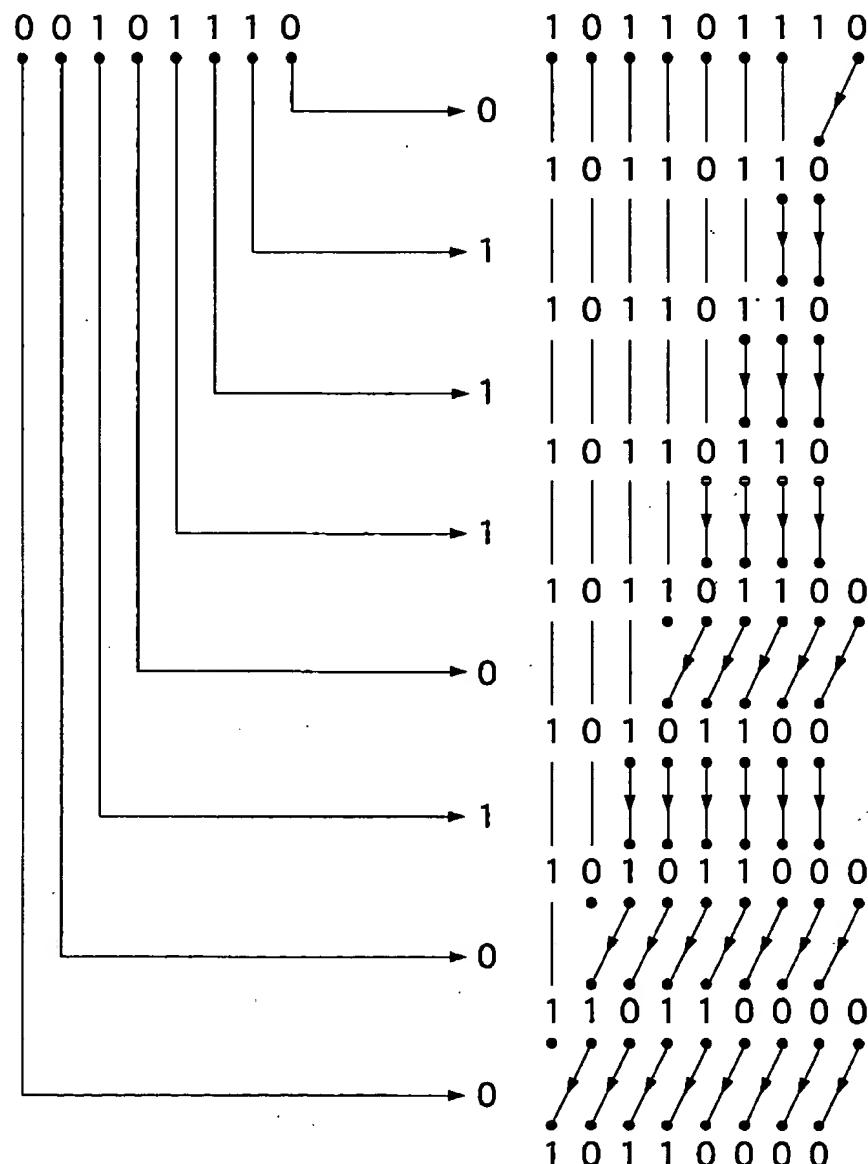
WHERE  $I_0 = 0$

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## F I G. 10

$$[b_7 \sim b_0] \\ = [0 0 1 0 1 1 1 0]$$

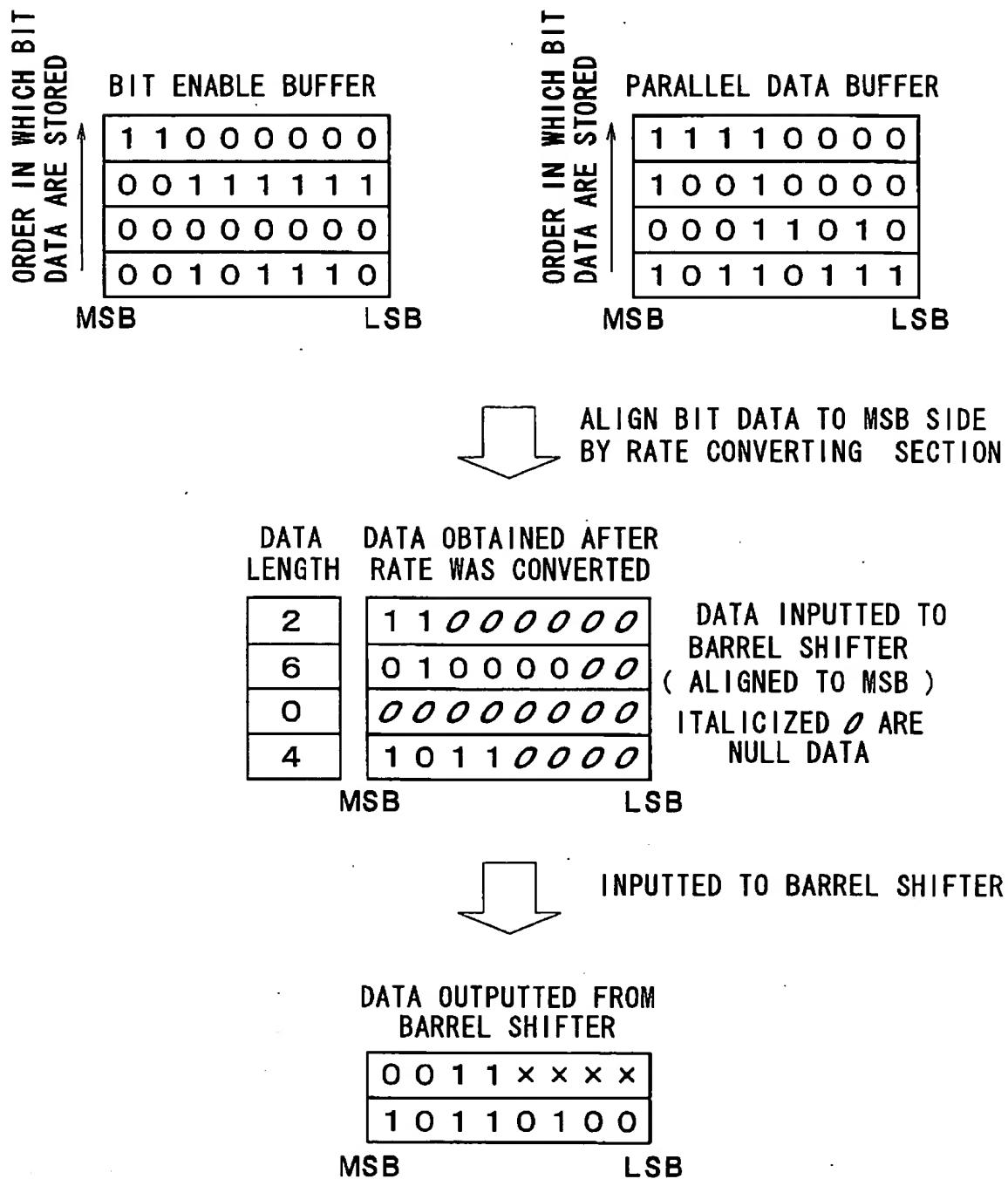
$$[a_7 \sim a_0] \\ = [1 0 1 1 0 1 1 1]$$



$$[c_7 \sim c_0] \\ = 1 0 1 1 0 0 0 0$$

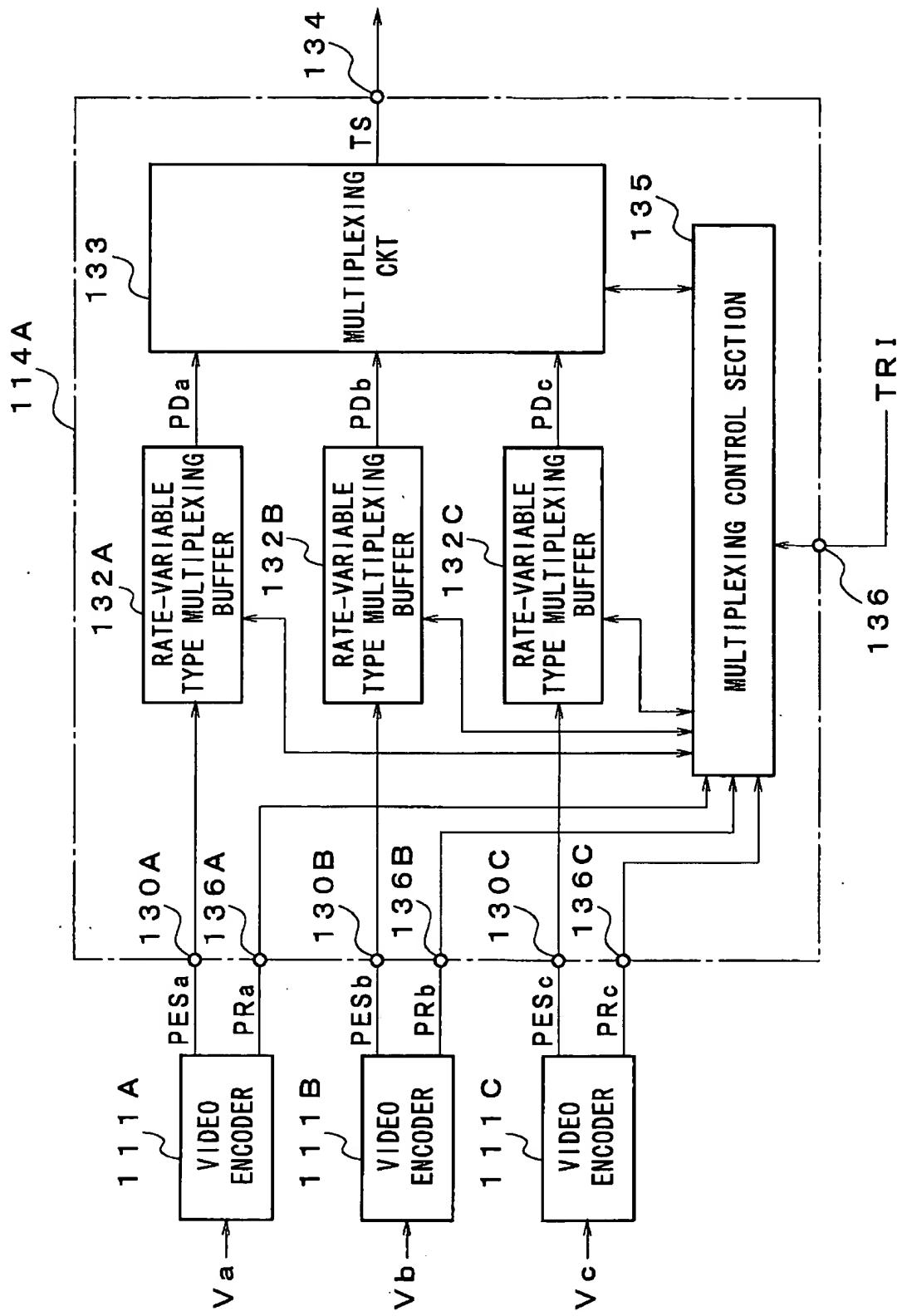
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### FIG. 11



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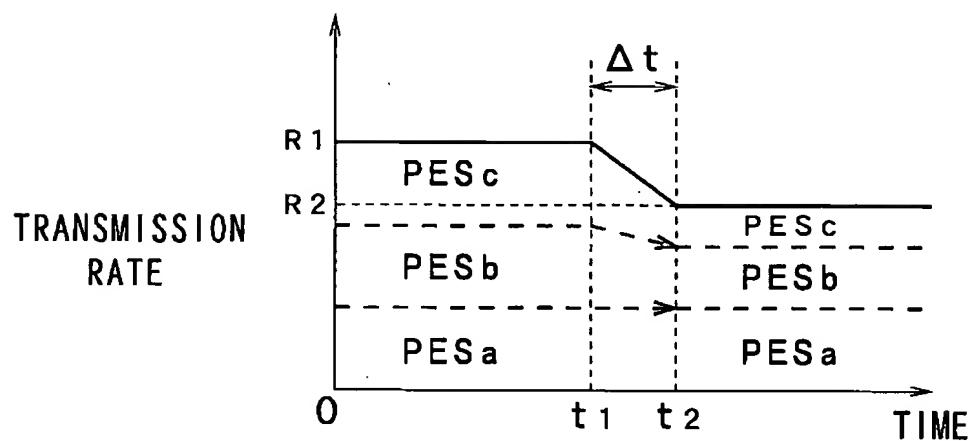
FIG. 12



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## FIG. 13

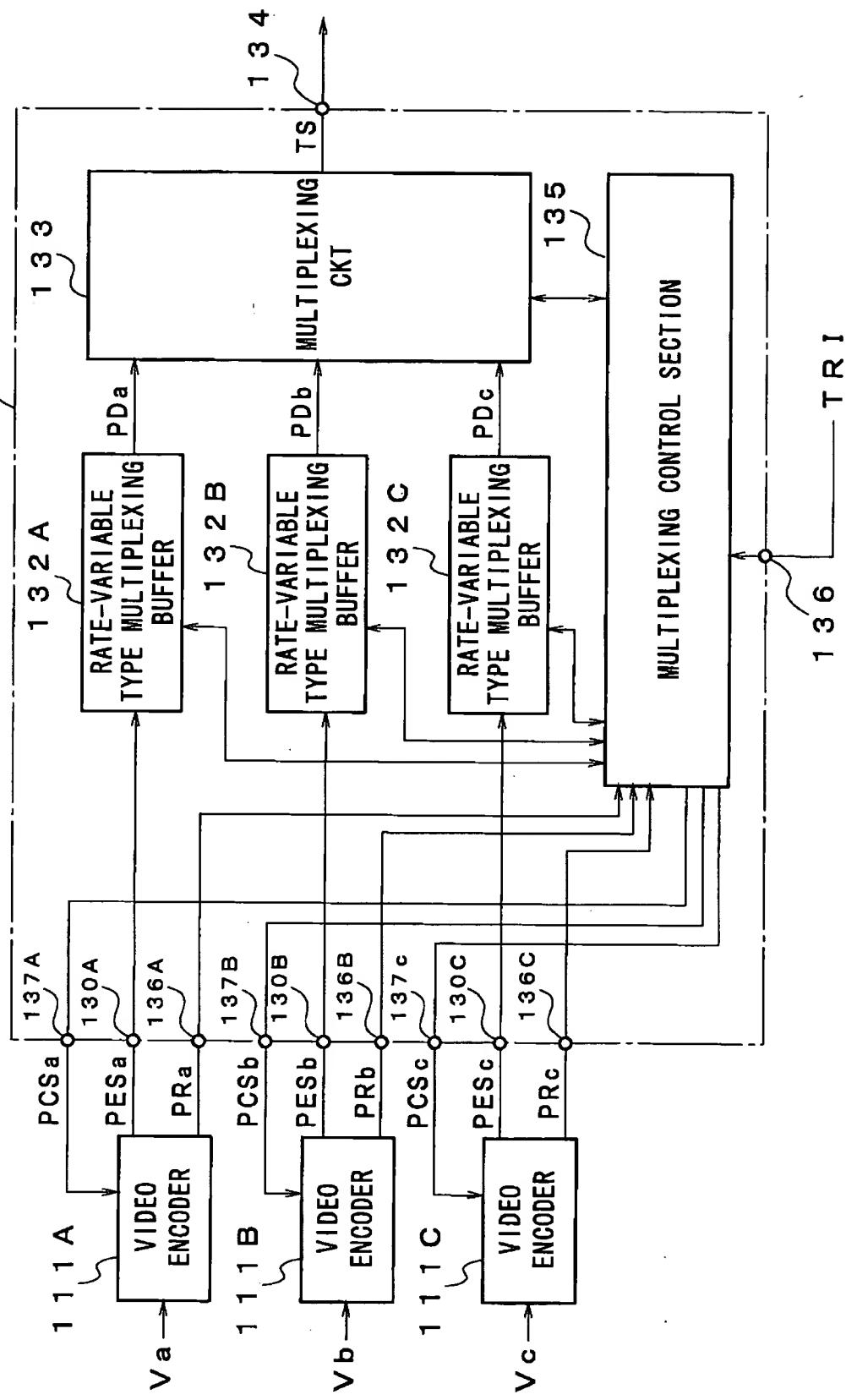
PRIORITY: PESa &gt; PESb &gt; PESc



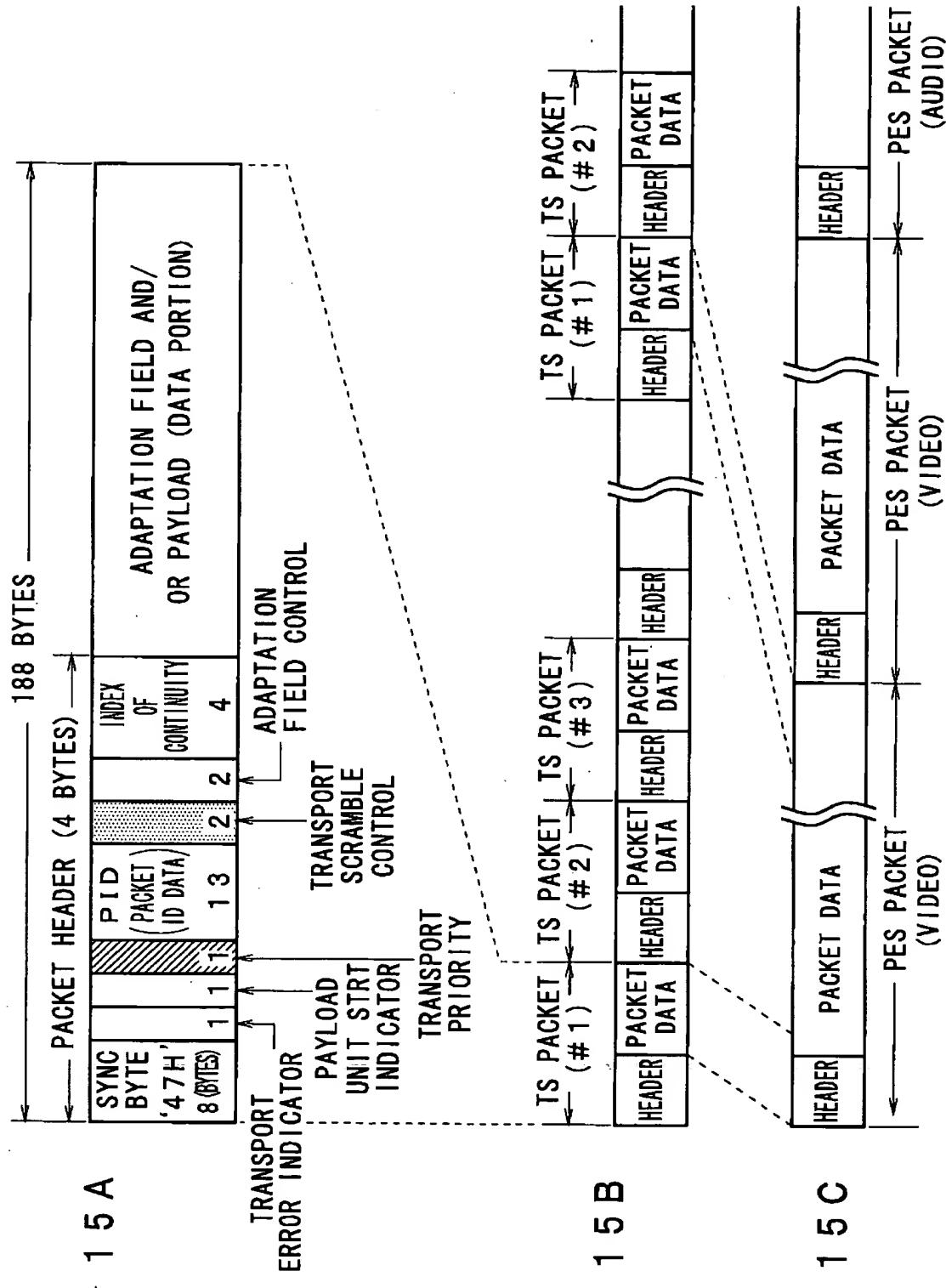
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FIG. 14

114B



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FIG. 16

